YICHEN WU

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655 Kelton Ave. Apt 414 • Los Angeles, CA 90024

EDUCATION

University of California, Los Angeles   **08/2013 ~ Current**

Expected Graduation Date: June 2017

Electrical Engineering major (Computer Engineering Option, Senior)

- **GPA: 3.8/4.0   (earned 14 A+'s)**
- **Coursework includes:**
  - Analog Circuit Analysis, Digital Signal Processing
  - Electromagnetics, Semiconductor Physics
  - Software Constructions, Computer Architecture, Operating Systems
  - Computer Network, Wireless Communication
  - Computer Graphics, Database Systems, FPGA Programming

TECHNICAL SKILLS

- C, C++, Java, ML, Prolog, Python, MATLAB
- SQL, Scala, LabVIEW, bash scripting, OpenGL
- Verilog, cadence virtuoso, Autodesk Inventor
- Platforms: Windows and Ubuntu
- Xilinx FPGA, Arduino, STM32 M3/4, LCDK(DSP)

EXPERIENCE

Research in Ozcan Lab at UCLA: **September 2016 ~ Present**

- CAD modeling an air tube that applies 1kV to capture particular matter and test air quality with holography imaging.

Projects: **June 2014 ~ Present**

- **DualCam 3D-Tracing Interactive System with Visual Display on FPGA**
  --Implemented 3D- tracing with dualCam on MicroBlaze-Core FPGA, and created a video game.
- **Bluetooth Remote Control App** -- Implemented communication and encryption protocol.
- **Self-balancing Two-wheeled Car** – Implemented pid control on stm32 platform.
- **Air-mouse** – Built handheld wireless (RF) mouse using gyroscope as control.
- **Phoneme Recognition** – Distinguished phoneme with MCFF feature extraction on DSP

SAIC Motor-Cp Co., Ltd., Thailand   **Summer 2014, 2 months**

Intern in Electrical Engineering Department

- Debugged wiring connection issues in assembling processes.
- Communicated with Design Release Engineers and helped form Change Requests
- Assisted following-up of suppliers of local parts including wiring harness, car battery, speakers, and antenna

HONORS & AWARDS

- Dean's Honor List   **Winter 2016, Fall 2015, Winter 2015, Fall 2014, Winter 2014**