

Deepak Ravikumar

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Education

University of California, Los Angeles
Bachelor of Science: Electrical Engineering, Biomedical Engineering Option

Expected July 2017

Skills

- Computer: Microsoft Office, XCode, Visual Studio, LabVIEW, Xilinx Vivado
- Experienced in C++ with knowledge of MySQL, Python, and PERL programming languages
- Language: Fluent in English, Proficient in Spanish and Tamil

Experience

Ozcan Research Laboratory | Los Angeles, CA

September 2016 – Present

Undergraduate Researcher

- Streamlined Arduino code for Tinyduino microcontroller that communicates with both a peristaltic pump and EXAR LED controller
- Investigating Circular Hough Transforms in order to identify small microorganisms such as E. Coli, Guardia Lamblia, and other water-residing microbes
- Researching machine learning and training set administration to develop high accuracy computer recognition of the above organisms

Facebook | Menlo Park, CA

June 2016 – September 2016

Technical Program Manager Intern

- Drove next generation Wedge switch program from conception to prototype. Led program through strict timeline, including Plan of Record presentation, while overseeing design, sourcing, and production validation planning across cross-functional teams
- Conducted research on the benefits and drawbacks of PCIe switching at the top of rack switch level as compared to the Ethernet standard today, with specific focus on power
- Aggregated Network Interface Card (NIC) information across the company, led weekly meetings with multiple vendors, and wrote a technical whitepaper on NICs including power and hardware perspectives

Pluribus Networks | Palo Alto, CA

June 2015 – August 2015

Hardware/Test Engineering Intern

- Implemented a read/write command to SFP/QSFP transceivers via the Pluribus Command Language (a mixture of Java and C) in the Freedom Spine and Leaf switch line in response to a client-found bug
- Conducted initial Zero Touch Provisioning testing for the F64 switch, specifically involving DHCP server protocol and MAC-IP address mapping
- Created testbeds to simulate active client topologies in deployment in-house and recreated and troubleshot bugs that customers reported
- Crafted an 18 minute presentation about the overall experience and delivered it to the entire company

Coursework

- *Computer Science*: Introduction to Computer Science I, Introduction to Computer Science II: Data Structures, Logic Design of Digital Systems, Introduction to Computer Organization
 - *Electrical Engineering*: Circuit Theory II, Circuits Laboratory II, Digital Signal Processing, Probability and Statistics, Analog Circuits
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